

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	26960	716/1-6.ccls. 714/1-57,100,724-745. ccls.	US-PGPUB; USPAT	OR	ON	2006/03/08 05:26
L5	2497	((HDL or VHDL or high adj2 level) same (RTL or register adj3 level))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/08 05:37
L6	64900	((fault\$3 or error or fail\$3 or defect\$4) same redundan\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/08 05:40
L7	606130	((add\$4 or insert\$4 or incooperat\$4 or implement\$4 or input or put\$4) same (fault\$3 or error or fail\$3 or defect\$4))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/08 05:42
L8	58942	((generat\$3 or implement\$4 or build\$4 or creat\$4 or construct\$4) same redundan\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/08 05:43
L9	29489	7 and 8	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/08 05:44
L10	20326	6 and 9	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/08 05:44
L11	79	5 and 10	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/08 05:44
L12	21	11 and (PLD or FPGA or programmable adj4 array)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/08 05:55
L13	17	1 and 12	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/08 05:54

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L14	5642	(cop\$3 or scan or duplicat\$4) and vot\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/08 05:55
L15	620	14 and 10	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/08 05:55
L16	179	1 and 15	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/08 06:03
L17	37	16 and (PLD or FPGA or programmable adj4 array)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/08 05:56
L18	4	5 and 17	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/08 06:03

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L1	26960	716/1-6.ccls. 714/1-57,100,724-745. ccls.	US-PGPUB; USPAT	OR	ON	2006/03/08 05:26
L3	1	I1 and ((HDL or VHDL or high adj2 level) and (RTL or register adj3 level) and (fault\$3 or error or fail\$3 or defect\$4) and redundan\$5 and (cop\$3 or duplicat\$3)).clm.	US-PGPUB; USPAT	OR	ON	2006/03/08 05:36
L4	1	((HDL or VHDL or high adj2 level) and (RTL or register adj3 level) and (fault\$3 or error or fail\$3 or defect\$4) and redundan\$5 and (cop\$3 or duplicat\$3)).clm.	US-PGPUB; USPAT	OR	ON	2006/03/08 05:36